

Fig. 1 - Combined V/T with 1 A/D

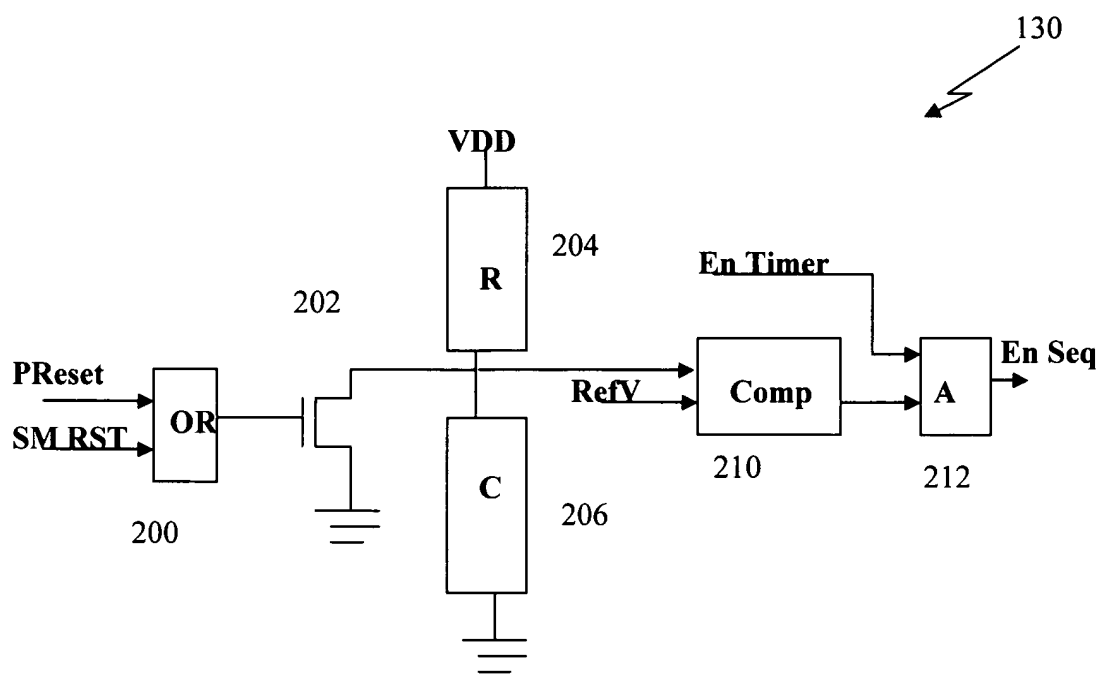
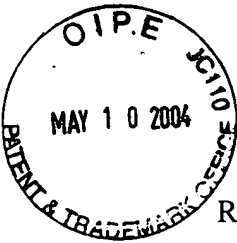
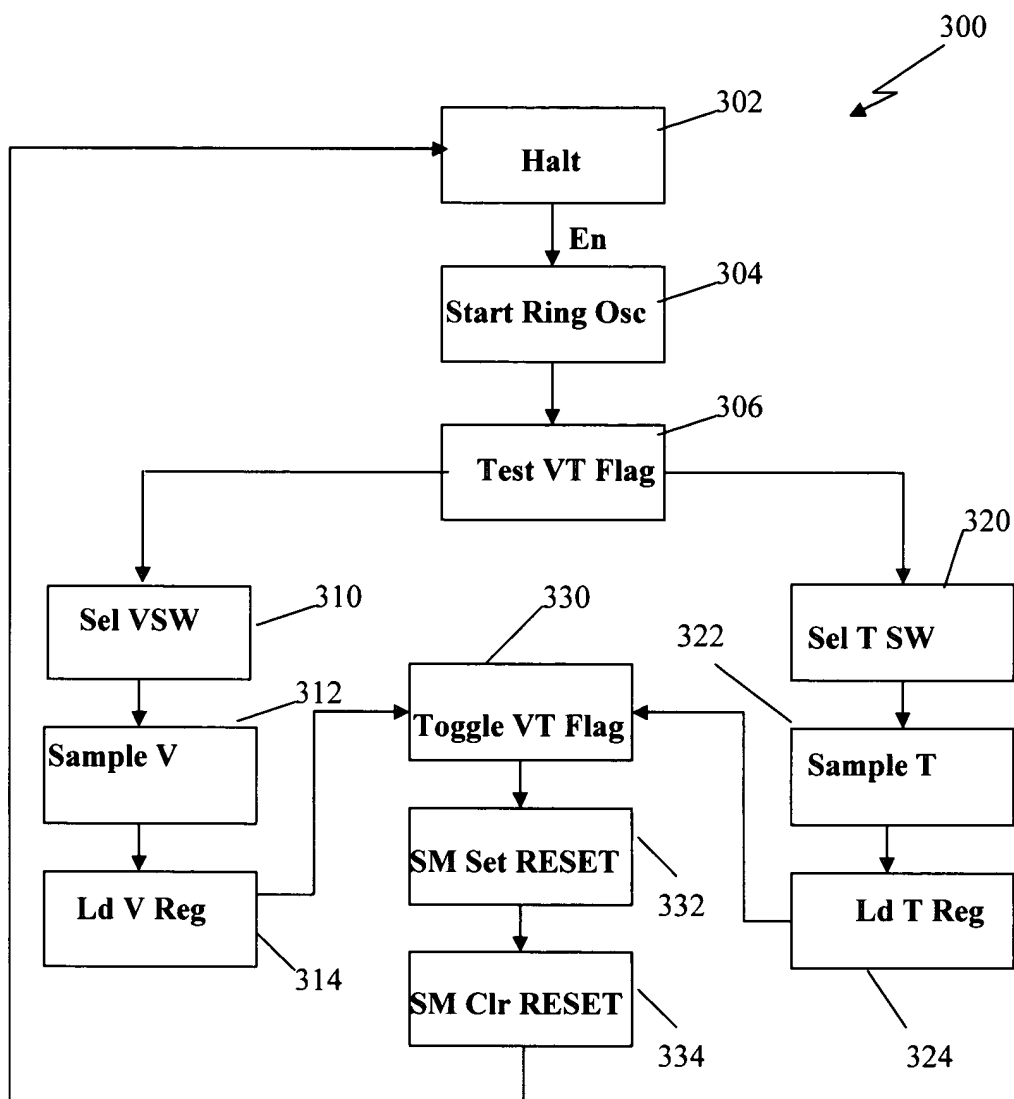


Fig 2 - RC Timer



**Fig 3 - SM Sequence**

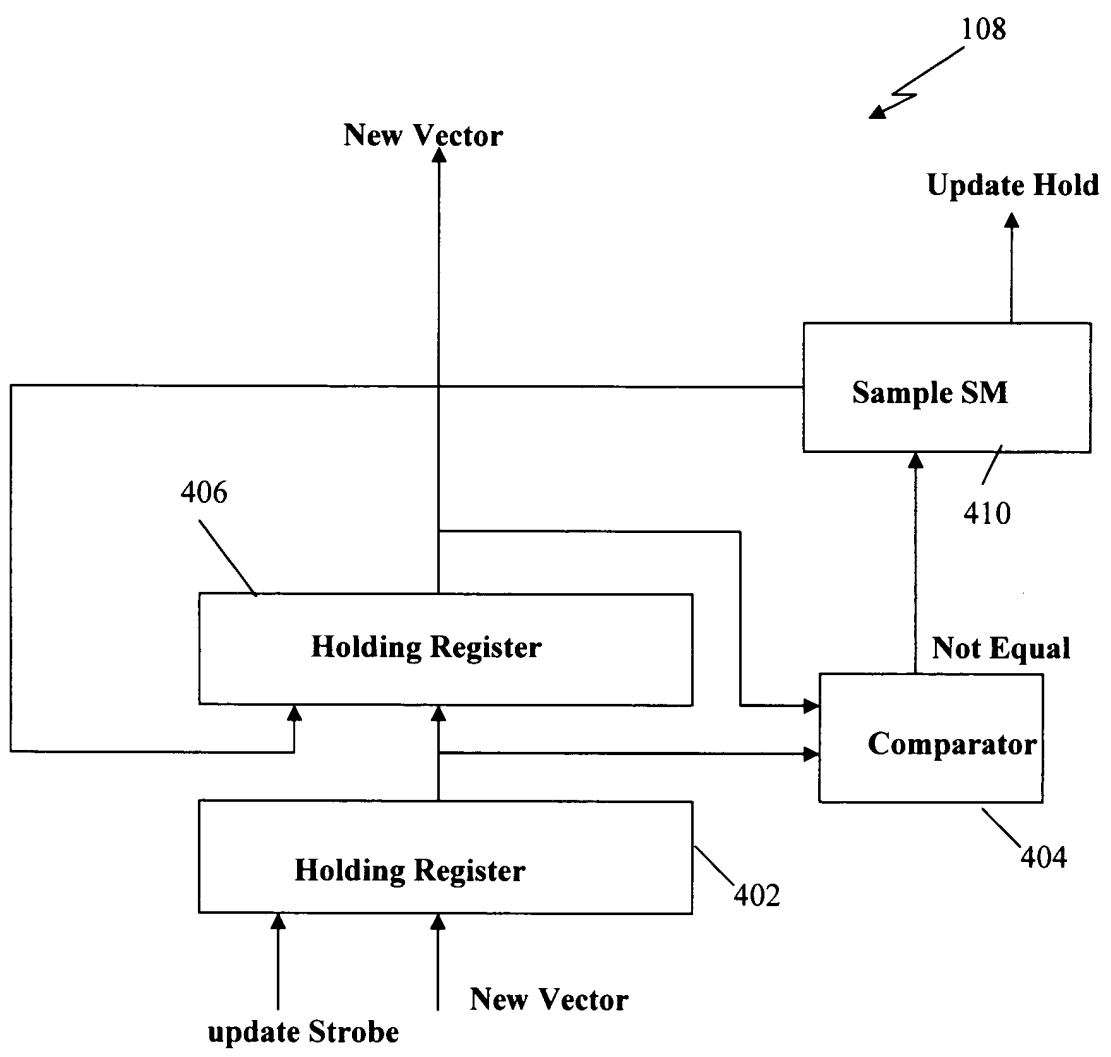
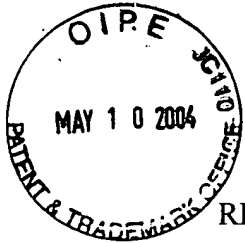
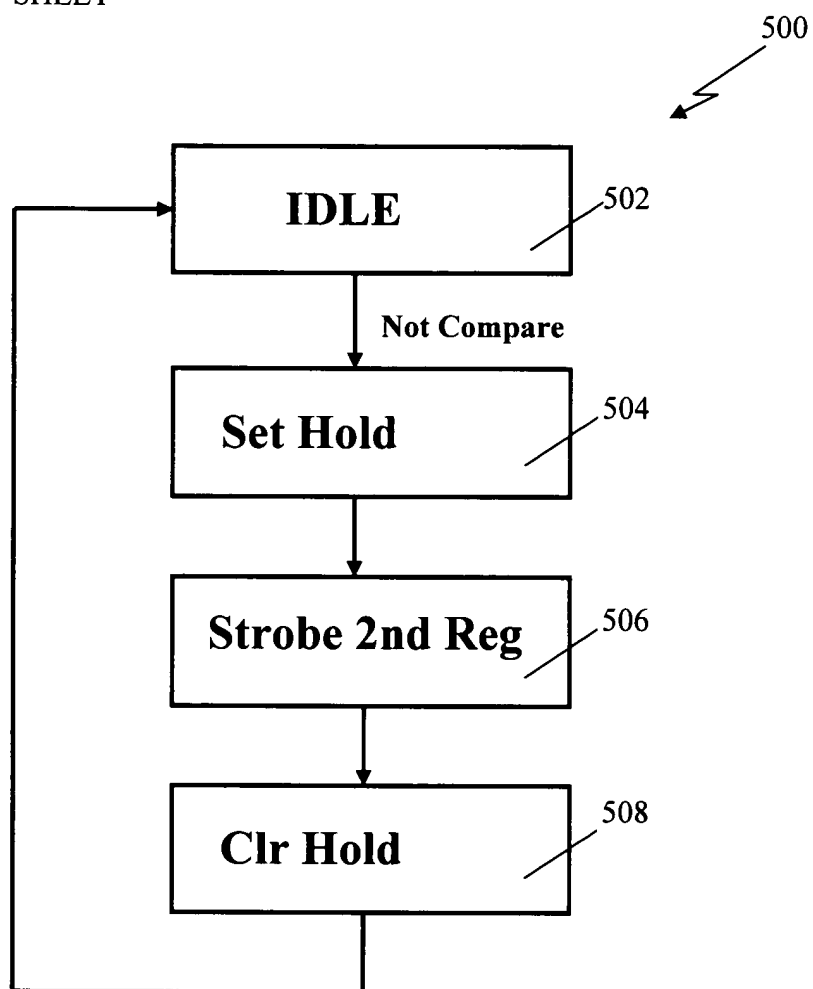


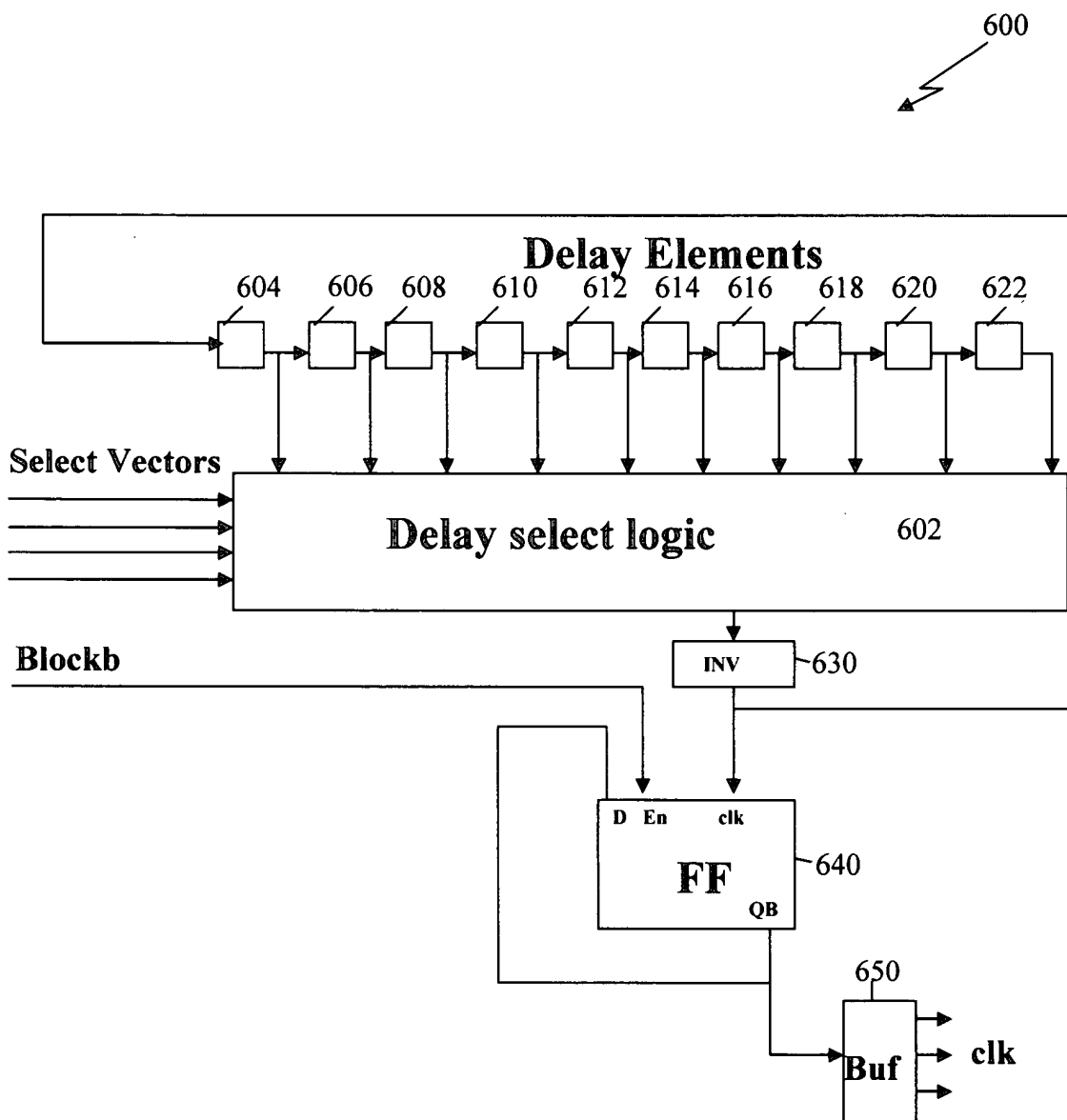
Figure 4 - SYNC/Sample



REPLACEMENT SHEET



**FIG 5 - Sync/Sample Sequence**



**Fig 6 - Adjustable Ring Oscillator**